

# With Actel, time is on your side.

*CAN'T SOMEONE'S FPGA  
GIVE US THIS??!*

*4.0ns CLOCK-TO-OUT  
0.6ns SETUP  
CUT 12 WEEKS TIME TO MARKET*

Compare your design requirements to these winning times:

0.25 ns Clock Skew at full load  
0.6 ns  $T_{su}$  (setup)  
4.0 ns  $T_{co}$  (clock-out)  
2.9 ns 32-bit decode internal  
7.7 ns 32-bit decode pin-to-pin

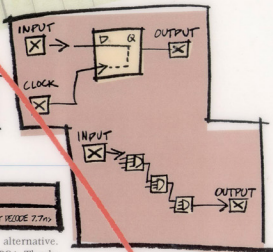
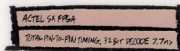
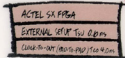
With a 40 percent performance advantage and densities up to 64,000 gates, Actel FPGAs are replacing gate arrays and multiple CPLDs in high-performance applications.

#### Introducing the Actel SX Family of FPGAs. The industry's fastest, high-performance programmable logic.

The Actel SX FPGA gives designers opportunities in high-performance, short-lifecycle markets where they once had few alternatives.

Gate arrays, because of their inherent high speed, are often used for performance-intensive designs. This, in spite of the fact that the process incurs high NRE charges and long prototype and production delays. When market timing is critical, these delays can be fatal.

The multiple high speed CPLD approach, while avoiding project-delay problems, has penalties of its own: high power consumption, high component cost, excessive board space, and design and debug complexity.



There is an alternative. The Actel SX FPGA. Thanks

to exceptional performance and high density, the SX can match the raw speed of an ASIC or be used to generate system wide savings by integrating up to 32 CPLDs into a single device.

#### The Actel SX. Now the industry's "ease-of-speed" leader.

What could matter most to designers in a time crunch is that the highly usable architecture of the SX requires no architecture-specific tweaking, or instantiation of structured HDL code to get the performance you need.

You hear a lot of chest beating in these electronic magazines. Instead of adding to the noise, we've included some real-world examples, and a way you can analyze SX capabilities on your project, on your desktop.

Read on! Now, you've got the time.

