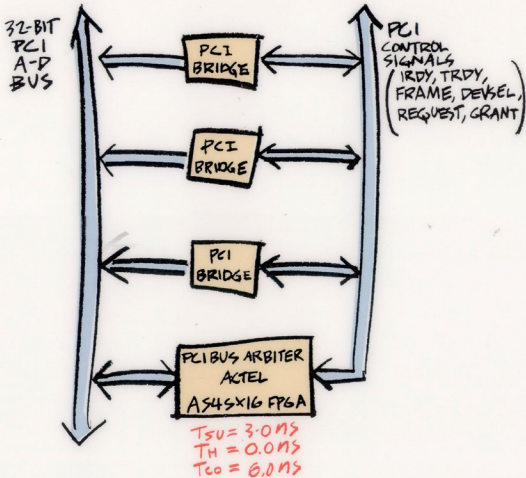


How a 0.6 ns input setup time and a fast 4.0 ns clock-to-out allowed implementation of a 66 MHz PCI Bus Arbiter for high-speed data networks.



One of the nation's leading internetworking equipment suppliers had a requirement for a PCI Bus arbitration circuit that had to operate at a sustained 50 MHz data rate. It also had to be compliant with the 66 MHz PCI specification for external timing (6.0 ns clock-to-out and 3.0 ns input setup time).

Of all the leading programmable logic suppliers aggressively competing for this business, only the Actel SX FPGA was capable of meeting the customer's specifications with its 4.0 ns clock-to-out and 0.6 ns input setup time.

For detailed information on how the SX can integrate high-speed PCI interfaces, call 1-888-99ACTEL or visit www.actel.com